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Combining STPA With SysML Modeling Enables SysML Models Simulation And Formal Verification Against Safety (using UPPAAL [21]), Security, And Performance Properties. TTool Supports AVATAR [14], A Customized SysML For The Modeling And Formal Verification Of Real-time Embedded Systems. For Simplicity, The Remainder Of The Paper Will Use 'SysML' And Not 'AVATAR'. Jan 16th, 2024

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