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### **EECE488: Analog CMOS Integrated Circuit Design**

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SM 3 EECE 488 – Set 1: Introduction And Background

References • Main Reference: Lecture Notes •

Recommended Textbook: Behzad Razavi, Design Of Analog CMOS Integrated Circuits, McGraw- Hill, 2001 •

Some Other Useful References: 23th, 2024

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EECE488 Analog CMOS Integrated Circuit Design

Assignment 4 Due: Tuesday November 26th, 2013 At

8:00am 1. Consider The Following Circuit. (This Circuit

Is Sometimes Referred To As A Self-biased Current

Source). R V DD GND M 0 M 2 M 1 M 3 GND 4 GND I

Out Assume All Transistors Are Operating In Saturation

Region And  $\beta = 0$ ,  $V_{DD} = 3\text{ V}$ ,  $V_{TH}(\text{NMOS}) = 0 \dots$  3th,

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### **Introduction And ...**

Analog CMOS Integrated Circuit Design ... R. Jac Ob B

Aker, CMOS C Ircut Design, Layut, And Simul Ti N, 3rd

... Circuit Design, E.g.,  $V_{TH}$  May Be Zero And The

Device May Be On For Any Positive Gate Voltage. •

Typically Threshold Voltage Is Adjusted By Ion

Implantation Into The 7th, 2024

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### **3. Single ...**

Common Source Basics - 1 • In Common-source

Amplifiers, The Input Is (somehow!) Connected To The

Gate And The Output Is (somehow!) Taken From The

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Two Groups: 1. Without Source Degeneration (no Body

Effect For The Main Transistor): 2. With Source

Degeneration (have To TakeFile Size: 751KB 7th, 2024

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Common Source Basics - 1 • In Common-source Amplifiers, The Input Is (somehow!) Connected Gate And The Output Is (somehow!) Taken From The Drain. To The • We Can Divide Common Source Amplifiers Into Two Groups: 1. Without Source Degeneration (no Body Effect For The Main Transistor): 2 10th, 2024

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