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Condition Mainly Used With Binary Logic Elements Where The Logic State 1 (TRUE) Is Converted To A Logic State 0 (FALSE) Or Vice Versa [IEC 60617-12, IEC 61082-2] 3.20 Logic Inversion Condition Mainly Used With Binary Logic Elements Where A Higher Physical Level Is Converted To A Lower Physical Level Or Vice Versa [Feb 15th, 2024

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KTH • Electric Power Systems Division • School Of Electrical Engineering • Teknikringen 33 • SE 100 44 Stockholm • Sweden Dr. Luigi Vanfretti • Tel.: +46-8 790 6625 • Luigiv@kth.se • Www.vanfretti.com Apr 3th, 2024

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Fundamentals Of Modern Protective Relaying

• All Voltage Phase Vectors Have Equal Amplitude • All Current And Voltage Vectors Have 120 Degrees Phase Shifts And A Sum Of 0. 3 Units 3 Units 3 Units 120° 120° 120 ° Non-Symmetrical System: • Fault Or Unbalanced Condition • If One Or More Of The Symmetrical System Conditions Is Not Met 3 Units 9 Units 4 Units 120° 100° 140° Feb 9th, 2024

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