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[SI],AL INC SI Next DEC DX JNZ Up DEC CX JNZ Again  
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– Using In/out Instruction To Store/restore SREG S2,  
2008 COMP9032 Week2 8 The Status Register In AVR  
(cont.) • Bit 7 – I: Global Interrupt Enable – Used To  
Enable And Disable Interrupts. – 1: Enabled. 0:  
Disabled. – The I-bit Is Cleared By Hardware After An  
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McGraw-Hill Series In Computer Organization And Architecture Author(S) John P. Hayes (Author) Publication Data New York: McGraw-Hill Book Company Publication€ Date 1987 Edition € 3rd Ed. Physical Description Xiv, 786 P. : Ill. ; 22 Cm. Subject Computer Subject Headings Digital Integrated Circuits Logic Design Microprocessors Microcomputers ... 7th, 2024

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## **MICROPROCESSORS AND MICROCONTROLLERS 3 0 0 3 OBJECTIVE ...**

Microprocessor/ Microcontroller Based System For A Given Control Application. ... "Microcomputer Systems: The 8086 / 8088 Family - Architecture, Programming And Design", Second Edition, Prentice Hall Of India, 2007. 3. A. K. Ray & K. M. Bhurchandi, "Advanced Microprocessors Nd A Peripherals- Architectures, Programming And Interfacing ... 5th, 2024

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