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Reversing Capacity Charge Pump Flying Capacitor Cflyi 0.022 0.047 -  $\mu\text{F}$  Starting Charge Pump Output

Capacitance  $CC_{POUT} = 1.0 \text{ to } 2.0 \text{ } \mu\text{F}$   $CC_{POUT} \geq C_{fly} * 10$   
Switching Regulator Oscillation Frequency  $F_{OSC1} = 700 \text{ to } 1.0 \text{ MHz}$   $R_{RT} = 82\text{k}\Omega \sim 180\text{k}\Omega$  Charge Pump  
Oscillation Frequency  $F_{OSC2} = 350 \text{ to } 500 \text{ to } 700 \text{ KHz}$   
 $R_{RT} = 82\text{k}\Omega \sim 180\text{k}\Omega$  Jan 7th, 2024

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7 DB3 Data Bit 3 D.3 8 DB4 Data Bit 4 D.4 9 DB5 Data Bit 5 D.5 10 DB6 Data Bit 6 D.6 11 DB7 Data Bit 7 D.7  
Table 2: Data Bits Pin Assignments The Only Real Reason That I Included The Pin Assignments Is That Throughout This Section Every Pin Assignment Will Be Displayed And Their Port Assignments In The Driver Will Be Clearly Understandable. Mar 7th, 2024

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Transmissive Display. The Path Of Light Is Described Below For The ON State Only In A Negative Image Display. • Light Enters The Panel Through The Rear Polarizer. At This Point, The Light Becomes Polarized To The Vertical Plane. • The Polarized Light Passes Unobstructed Th Apr 3th, 2024

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Manuel D'installation English Mar 18th, 2024

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Introduction To LCD Driver IC • Improved Visualization:  
- A Fundamental Market Enabler 98(□) NCHU / EE / □□□/ Fansen@dragon.nchu.edu.tw Page 4 / 195 Driving Circuits Of TFT- LCD Module Connector Connector DC/DC Converter DC/DC Converter LVDS Receiver LVDS Receiver Timing Controller Timing Controller



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