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 $A + B =$ Shift Register Shift Register Adder FSM Shift
 Register B A A B S Clock Example Of The Serial Adder
 $A = A_{N-1} A_{N-2} \dots A_0$ $B = B_{N-1} B_{N-2} \dots B_0$ $A = S$
 $N-1 S_{N-2} \dots S_0 = A + B$ Apr 13th, 2024

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2007. Ferenc Kovac Has Been (and Will Continue To
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