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The CPU Like ALU, Control Unit, Various Registers, Multipliers And Buses. The Verilog HDL Code For The Datapath Includes Interconnection Of The Various 3.2 ALU The Arithmetic Logical Unit (ALU) We Have Designed Is A Very Simple One. Its Functions Include Basic Arithmetic 3th, 2024

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The Information Is Grouped Into 8-bit Data Packages Called Bytes. A Pair Of Bytes Is Called A Word (16-bit); A Pair Of Words

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