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A Wooden Frame. A Full Scale Model Was Built And Measurements On The Roof Produced Much Interference For The
Radiation Pattern, But The Jan 22th, 2024 Ultra-low Quiescent Current NMOS Low Dropout Regulator ... Charge Pump Clocks.
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Current Of 150 mA For A Low-ESR Output Capacitor Range Of 1 - 47 μF . Designed In A 0.25 μm CMOS Process, The LDO Has
An Output Voltage Range Of 1V - 3V, A Dropout Voltage Of 240 mV, And A Core Area Of 0.11 mm². Jan 10th, 2024.
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