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The FM Tuner Is Used To Remove Any Amplitude
Modulation Of The FM Signal. See Fig 4. The Limited
Signal From V8 Is Applied Through Transformer T8, To
Ratio Detector Tube V9A (6BN8). The Ratio Detector
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AmplifierDesigner.4 In Designer, Enter 50 W For
Source Resistance, 110W For Load Resistance, 52 MHz
For Frequency, And Desired Q Of 2. Source Resistance
Is The Exciter Output Impedance, Load Resistance Is
The 3-500Z Input Impedance, And Q Determines
Bandwidth Of The Circuit. After Modeling The Ci 4th,
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INTRODUCTION ... Because Of The Sequence Of Digital Logic Involved, The Clock Will Be Explained First.

CLOCK The Clock Consists Of Transistors Q1, Q2, And Their Associated Circuitry. When IC1 Pin 8 Is High The Clock Is Disabled. Capacitor C3 Charges Through Diode D1 And Biases Q1 Off. With Q1 Off, The Base 22th, 2024TATOG - Classic Heathkit ComputersHeathkit Computer Users Are Eligibleior HUG, The Heath User's Group, Heathkit H11 Customers Are Also Eligible To Join DECUSTM, The Digital Equip- T Ment Corporation User's Society. We're.conf Ident You'll Find The Heathkit Computer Line One Of The Most Intelligent, Sensibly Developed And Com-plete Product Lines Available Today. Lt Offers You Total 3th, 2024.

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