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Solution: (a) Instruction Cycle Time Is $1/(4 \text{ MHz}) = 250 \text{ ns}$ (b) Instruction Cycle Time Is $1/(8 \text{ MHz}) = 125 \text{ ns}$ (c) Instruction Cycle Time Is $1/(10 \text{ MHz}) = 100 \text{ ns}$ Example 8-1. Fuse Bits And Reset Delay The Most Difficult Time For A System Is During Power-up. The CPU Needs Feb 9th, 2024

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