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Engineer-to-Engineer Note EE-265 - Analog Devices Signals (PCM\_CLK, PCM\_SYNC, PCM\_OUT, And PCM\_IN). There Are Three Modes Of Operation For PCM: (1) Short Frame Sync Mode (2) Long Frame Sync Mode (3) Multi-slot Mode The Timing And Description Of All Three Modes Are Discussed Below. In Short Frame Sync Mode (Figure 1), The Falling Edge Of PCM\_S Mar 3th, 2024  
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(EE-372)[1] To Learn How To Begin Development With CCES. Blackfin+ Core Overview The First Step In Writing Efficient C/C++ Code Is To Understand The Processor On Which A Developer Is Working. Jan 7th, 2024  
Engineer To Engineer Note EE-167 - Analog Devices Contributed By Maikel Kokaly-Bannourah April 04, 2003  
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