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Each Node. E.g., At Node B, I3 + I6 + I4 = 0. (We Have Followed Jan 2th, 2024EE101: Op Amp Circuits (Part 2) - IIT BombayCommon-mode And Di Erential-mode Voltages Amplifier R C R D R A R B V CC V O V 1 V 2 Consider A Bridge Circuit For Sensing Jan 2th, 2024EE101: Diode Circuits* V