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Converters, CPU, DSP, ASIC, FPGA Advantages: $\frac{3}{4}$ noise Is Easy To Control After Initial Quantization $\frac{3}{4}$ highly Linear (with Limited Dynamic Range) $\frac{3}{4}$ complex Algorithms Fit Into A Single Chip $\frac{3}{4}$ flexibility, Parameters Can Be Varied In Software
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Discrete Time Signal Processing Computer (Figure 1.1(b)). These Represent A Continuous-time, Discrete-time And Digital Signal Respec-tively. As We Know From The Sampling Theorem, The Continuous-time Signal Can Be Reconstructed From Its Sam-ples Taken With A Sampling Rate At Least Twice The Highest Frequency Component In The Signal Mar 20th, 2024.

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