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VHDL = VHSIC Hardware Description Language (VHSIC = Very High Speed Integrated Circuits) Developed By DOD From 1983 -based On ADA Language IEEE Standard 1076-1987/1993/2002/2008 Gate Level Through System Level Design And Verification Verilog -created In 1984 By Phil Moorby And Prabh Mar 2th, 2024Digital System Design With PLDs And FPGAs VHDLKuruvilla Varghese A B C Y Z. 11 Multiple Processes, Concurrency 21 Eqproc:process (a, B, C) Begin 0 End Process Eqproc; Eqproc:process (a, D, E) Begin 0 End Process Eqproc; Kuruvilla Varghese A B C U V A D E Y Z Hierarchy 22 Kuruvilla Varghese Component Signal O U T P U T S I N P U T S Apr 2th, 2024.

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