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Palnitkar, Verilog HDL: A Guide To Digital Design And Synthesis, Prentice Hall, 1996.
• M. Ciletti, Modeling, Synthesis, And Rapid Prototyping With The 23th, 2024

CS 135: Computer Architecture I Digital Logic Circuits

6 CS 135 Ok....start Building Logic Gates • Use N Type And P Type Transistors •
'signal' Is A 1 Or 0 And Nothing Else • Output Value Will Be Voltage Measured At
Some Point In The "circuit" $\frac{3}{4}$ Need To Determine Where To Designate The Output
Point (i.e., Where To Measure) • Inputs Will Be Applied To The Transis 19th, 2024

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