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A 5.3 GHz 16b 1.75 GS/S Wideband RF Mixing-DAC Achieving IMD The Proposed Architecture Is Implemented Using A Triple-

well 65nm CMOS Technology With 1.2V And 3.3V Supplies. A Micrograph Of The Die Of The Dual-Mixing-DAC With Two Mixing-DAC Cores And The Digital Front-end Is Shown In Fig. 9.6.7. The Area Of One Mixing-DAC Core Is 1.6mm^2 . The Nominal Full-scale Output Current Is 20mA In A Differential 50load. Jan 6th, 2024

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