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Computer Organization
Registers
Addressing
Buses
Fetch-Execute Cycle Computer Organization
Control Unit ALU Registers Memory CPU Input Output
Components
Control Unit
Fetches Instructions,
Decodes Instructions, Causes Instructions T Mar 3th,
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[JPH] John P Hayes. Computer Architecture And Organization. 3e. McGraw Hill. 1998. [MMM-CSA] M. Morris Mano. Computer System Architecture. 3e.

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