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MOS Theory, SPICE, Fabrication Introduction To Digital Integrated Circuit Design Lecture 2 - 3 Recommended Reading J. Rabaey Et. Al. "Digital Integrated Circuits: A Design Perspective": Chapter 2 (2.1 - 2.3), Chapter 3 (3.3) Weste And Harris, "CMOS VLSI Design: A Circuits And Systems Perspective": Chapter 2, Chapter 3 (3.2), Chapter 5. 1th, 2024

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Terminals Of An Op Amp Is The Differential Input Resistance  $R_{id}$ . 5. Differential Gain At DC ( $A_{0d}$ ) And Common-Mode Rejection Ratio (CMRR). The Output Voltage Of An Op Amp At Dc Can Be Expressed As  $V_3 = A_{0d}(V_2 - V_1) + A_{0cm} \frac{V_1 + V_2}{2}$  (B.2) Where  $A_{0d}$  And  $A_{0cm}$  Are, respectively, the differential and common-mode gain of the Op Amp At Dc. For An Op ... 2th, 2024

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B.1.2 The Diode Model B-4 B.1.3 The Zener Diode Model B-6 B.1.4 MOSFET Models B-6 B.1.5 The BJT Model B-10 B.2 SPICE Examples B-13 S.2.1 Performance Of A Noninverting Amplifier B-13 S.2.2 Characteristics Of The 741 Op Amp B-16 S.4.1 Design Of A DC Power Supply B-19 S.6.1 Dependence Of The BJT.  $\beta$  On The Bias Circuit B-24 S.7.1 The CS Amplifier B-25 1th, 2024

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SPICE QDC Analysis QTransient Analysis QSubcircuits QOptimiza 3th, 2024

### **SPICE-Simulation Using LTspice IV**

13. Project 9: Echos On Transmission Lines 81 13.1. Transmission Lines -- Only Two Wires? 81 13.2. Echoes 83 3. Simulation Of The Example With LTspice 85 13. 13.4. Open Or Short Circuit At Cable' 3th, 2024

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8: SPICE Simulation CMOS VLSI Design CMOS VLSI Design 4th Ed. 3 Introduction To SPICE Simulation Program With Integrated Circuit Emphasis – Developed In 1970's At Berkeley – Many Commercial Versions Are Available – HSPICE Is A Robust Industry Standard • Has Many Enhancements That We Will Use Written In FORTRAN 1th, 2024

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AC Analysis Of A Circuit Should Be Carried Out In The Steady State. To Prepare Ac Analysis, Run Transient Simulation Of The Circuit Until It Reaches The Steady State. In This Example, With The Duty Cycle Of 0.6, The 2th, 2024

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