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213 Architecture View: Memory, Disks, CPU, Cache/memory Hierarchy

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17 Photo Of A Punch Card For The Hollerith Machine, From John McPherson, Computer Engineer, An Oral History Conducted In 1992 By William Aspray, IEEE History Center, Rutgers University, New Brunswick, NJ, USA. Mar 4th, 2024.

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Passion For Computing, And This Inspired Me To Continue My Studies In This field. I Was Not Too Surprised When I Met Mihai And Raluca Again, Years Later, In The Graduate Program At Carnegie Mellon University. Among My Professors At The “Politehnica” University In Bucharest Were Mircea Pe-

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