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And Output Common Mode Of 650mV Is Used For All Differential Lines, And A Supply Voltage Of 900mV. All Differential Lines Keep The Transistors In Saturation With Input Common Mode Of 900mV Down To 400mV. 4. Equalization Sub-Blocks A. Binary Weighted Current DAC Figure 3 The Tx FIR, Rx D Feb 11th, 2024

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