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Allmän ...Condition Mainly Used With Binary Logic Elements Where The Logic State 1 (TRUE) Is Converted To A Logic State 0 (FALSE) Or Vice Versa [IEC 60617-12, IEC 61082-2] 3.20 Logic Inversion Condition Mainly Used With Binary Logic Elements Where A Higher Physical Level Is Converted To A Lower Physical Level Or Vice Versa [ Apr 10th, 2024Aspire Public Schools - Aspire Centennial College ...Aspire Public Schools - Aspire Centennial College Preparatory Academy Page 2 Of 18 . 50% Of This Project Cost Through A Signed Funding Agreement With CSFA. This Agreement Has A 30-year Term To January 2037 With Annual Payments Of \$231,795 Based On A Final Interest Rate Of 2.202%. Additionally, Aspire Has Received A Preliminary Apportionment Of Apr 3th, 2024.

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