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- The Instruction Tell The CPU What To Do, While Directives (pseudo Instructions) Give Direction To Assembler. - Directives: EQU, SET, ORG (Origin), END, LIST -Directive Don't Generate Any Machine Code And Used By Assembler. EQU (equate) O It Is Used To Define A Constant Value Or A Fixed Address. 18th, 2024

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Template For A MIPS Assembly Language Program: # Comment Giving Name Of Program And Description Of Function # Template.s # Bare-bones Outline Of MIPS Assembly Language Program.data # Variable Declarations Fo 14th, 2024

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WRITE; Indicates The Data On The Data Bus Is To Be Written Into The Selected Memory Or 1/0 Location. Data Is Set Up At The Trailing Edge Of WR. 3 Stated During Hold And Halt Modes. READY (Input) If Ready Is High During A Read Or Write Cycle, It Indicates That The Memory Or Peripheral Is Ready To Send Or Receive Data. 29th, 2024

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2 Outline 2.1 Inside The 8051 2.2 Introduction To 8051 Assembly Programming 2.3 Assembling And Running An 8051 Program 2.4 The Program Counter And ROM Space In The 8051 2.5 8051 Data Types And Directives 2.6 8051 Flag Bits And The PSW Register 2.7 8051 Register Banks And Stack 11th, 2024

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