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Condition Mainly Used With Binary Logic Elements Where The Logic State 1 (TRUE) Is Converted To A Logic State 0 (FALSE) Or Vice Versa [IEC 60617-12, IEC 61082-2] 3.20 Logic Inversion Condition Mainly Used With Binary Logic Elements Where A Higher Physical Level Is Converted To A Lower Physical Level Or Vice Versa [May 2th. 2024

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Achieved. This Is An Important Step Towards A "software-radio" Which Can Be Used In Various Systems. The DDS Could Be Applied In The Modulator Or Demodulator In The Communication Systems. The Applications Of DDS Are Restricted To The Modulator In The Base Station. Mar 1th. 2024

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