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This Glossary Was Compiled To Promote A Jan 8th, 2024.

Digital PID Controller DesignDigital PID Controller DesignDigital PID Controller Design 2 Let T 1;¢¢¢;t K Denote The Real Distinct Zeros Of T(u;½)ofodd Multiplicity, For U 2 (i1;1), Ordered As Follows: i1 < T 1