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Integrated Circuits And Systems, DOI 10.1007/978-1-4419-7618-5_9, ... Designing NoCs For 3D Chips That Are Application-specific, With Minimum Power-delay Is A Major Challenge. Successful Deployment Of NoCs Require Dedicated So- ... The Yield Of A 3D IC Can Be Affected By The Number Of TSVs Used, Depending On The Technology. In Fig. 9.4, We Show ... Apr 19th, 2024

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Computers, Smartphones And Sensors (Jammal Et Al., 2014). Answering These Exponential Demands Dynamically And Efficiently, Is One Of The Biggest Challenges For The Network Management In Current Networks (Jammal Et Al., 2014). A Considerable Effort To Address These Iss Mar 7th, 2024

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