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97403-1202 Roblim1@cs.uoregon.edu Abstract Tuning Codes For GPGPU Architectures Is Challenging Because Few Performance Tools Can Pinpoint The Exact Causes Of Execution Bottlenecks. While Profiling Applications Can Reveal Execution Behavior With A Particular Architecture, The Abundance Of Apr 8th, 2024

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Architecture Increase Linearly, Resulting In Convolution Taking Up An Even Higher Share Of The Total Time. 3. Group The Pixels Of The Input Image Into “cells” Based On A Cell Width (commonly Referred To As “sbin” In HOG Literature)  $C$ . To Compute The Histogram At Each Cell We Consider Contributions From All Pixels From The Cell. Feb 6th, 2024

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