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Serial EEPROM Cross Reference Guide SERIAL EEPROM CROSS REFERENCE GUIDE

Size (bits)	MCHP Part #	Atmel Part #	Catalyst Part #	Fairchild Part #	Philips / Signetics Part #	Rohm Part #	ST Part #	Xicor Part #
128 To 1024K I2C™ Devices								

(Microchip's B Revision Devices Do Not Use Address Pins A0, A1 And A2. These Pins Have No Mar 21th, 2024)

PIC16F84A 18-pin Enhanced Flash/EEPROM 8-Bit MCU Data Sheet

Devices Included In This Data Sheet:

- PIC16F84A • Extended Voltage Range Device Available (PIC16 LF 84A)

High Performance RISC CPU Features:

- Only 35 Single Word Instructions To Learn
- All Instructions Single Cycle Except For Program Branches Which Are Two-cycle
- Operating Speed: DC - 20 MHz
- Clock Input DC - 200 ns Instruction Cycle

Feb 20th, 2024

PIC16F8X, 18-Pin FLASH/EEPROM 8-Bit MCU Data Sheet

Identification System" At The Back Of This Data Sheet To Specify The Correct Part Number. There Are Four Device "types" As Indicated In The Device Number.

1. F, As In PIC16F84. These Devices Have Flash Program Memory And Operate Over The Standard Voltage Range.
2. LF, As In PIC16 LF84.

Jan 15th, 2024.

PIC16F84A 18-pin Enhanced Flash/EEPROM 8-Bit MCU Data ...Data EEPROM Memory Have The Address Range 0h-3Fh. More Details On The EEPROM Memory Can Be Found In Section 5.0. Additional Information On Device Memory May Be Found In The PICmicro™ Mid-Range Reference Manual, (DS33023).

2.1 Program Memory Organization

The PIC16FXX Has A 13-bit Program Counter Capable Of Jan 20th, 2024

1-Mbit Serial I²C Bus EEPROM - STMicroelectronics Description M24M01-R M24M01-DF 6/47 DocID12943 Rev 14 1 Description The M24M01 Is A 1 Mbit I2C-compatible EEPROM (Electrically Erasable PROgrammable Memory) Organized As 128 K × 8 Bits. The M24M01-R Can Operate With Apr 29th, 2024

32-Kbit Serial SPI Bus EEPROM With High-speed Clock See Section 10: Package Information For Package Dimensions, And How To Identify Pin-1.

633 \$ # 1 (/,\$ 36## 7!) \$- XXX

Memory Organization

M95320-W M95320-R M95320-DF 8/48 DocID5711 Rev 16 2

Memory Organization

The Memory Is Organized As Shown In The Following Figure. Figure 3. Block Diagram 06 Feb 13th, 2024.

1K Microwire Compatible Serial EEPROM

© 2002-2013 Microchip Technology Inc. Ds20001749k-page 5 93aa46a/b/c, 93lc46a/b/c, 93c46a/b/c Figure 1-1:

Synchronous Data Timing Table 1-3: Instructio

Apr 8th, 2024

EEPROM Cross Reference List

AT93 Atmel Non-Volatile Memory, 3-wire Serial Bus Interface C C = CMOS XX 46 = 1K, 56 = 2K, 57 = 2K With Shorter Address, 66 = 4K Blank/A/C Blank = X8 Or X16 Organization, A = X16 Org., C = X16 Org. With Schmitt Trigger Inputs Blank/R/W Blank = Normal Pin Out, R = Rotated Die P Feb 23th, 2024

Basic Serial EEPROM Operation

Microchip's Default Conditions For The 3-wire And 2-wire Serial EEPROMs To Allow The System Designer To Utilize The Benefits Of Serial EEPROMs.

3-WIRE BUS OPERATION PRIMER

Many Serial EEPROM Data Sheets Are Written In A Conventional Memory Data Sheet Format Which Empha-sizes The Featu Feb 9th, 2024.

ROM, EPROM, And EEPROM Technology Of ROM Architectures (NOR, NAND, Etc.) Are Detailed In The Flash Memory Section (Section 10) As They Use The Same Principle. Figure 9-3 Shows An Array Of Storage Cells (NAND Architecture). This Array Consist Mar 14th, 2024 18-pin Flash/EEPROM 8-Bit Microcontrollers Contact Your Microchip Regional Sales Office. 2.4 ROM Devices Some Of Microchip's Devices Have A Corresponding Device Where The Program Memory Is A ROM. These Devices Give A Cost Savings Over Microchip's Traditional User Programmed Devices (EPROM, EEPROM). ROM Devices (PIC16CR8X) Do Not Allow Serializati Feb 21th, 2024 PIC16CE62X OTP 8-Bit CMOS MCU With EEPROM Data Memory 1999 Microchip Technology Inc. DS40182C-page 3 PIC16CE62X 1.0 GENERAL DESCRIPTION The PIC16CE62X Are 18 And 20-Pin EPROM-based Members Of The Versatile PICmicro ® Family Mar 28th, 2024.

EEPROM Emulation For Gen 2 C2000 Real-Time MCUs (Rev. A) Figure 4-1 Shows The Directory Structure Of The Generation 2 C2000 MCUs_EEPROM Directory. Figure 4-1. F28xxx_EEPROM Directory Structure As Shown, Each F28xxx Generation Has Its Own Folder That Contains Subfolders. The Flash API And Header Files For Each Generation Are Provided. The Example Jan 14th, 2024 CE27 EEPROM PROGRAMMING SOFTWARE REFERENCE ANUAL The CE27 Is Used To Program The VXR-7000 Desktop Repeater. With The CE27 Programming Software, You Can Quickly And Easily Program The Vertex VXR-7000 Repeater's Channels And Configuration From Your Personal Computer. In The Event Of An Accidental Memory Failure, Re-peater Memory And Co Mar 12th, 2024 D2XX EEPROM Programming Examples - FTDI FTDI's D2XX Drivers Allow For Users To Read And Program The EEPROM Directly. Refer To D2XX Programmer's Guide Section 4 EEPROM Programming Interface Functions. The D2XX Drivers Should Be Installed On The PC For These Examples To Work. These Programming Functions Could Be Used In A Production Environment. 1.2 Scope Feb 21th, 2024.

ESD Testing Of An EEPROM-Based Multichip Module Device Was Proven To Be A Class II Device Per MIL-STD-1686C (Human Body Model Or HBM). The MCM Circuit The MCM Circuit Was Composed Of Eight Hitachi HN58C1001 EEPROMs And Three UTMC UT54ACT244 Uni-directional Buffer Chips (see Attachment To This D Feb 9th, 2024 EEPROM Emulation In STM32F0xx Microcontrollers 256 Elements (1- Kbyte Page) Page 0 Page 1 Var1 Virtual Address 5555h Var2 Virtual Address 6666h Var3 Virtual Address 7777h. AN4061 Implementing EEPROM Emulation Doc ID 022893 Rev 1 9/19 Figure 3. D Apr 30th, 2024 12-Bit DAC With EEPROM Memory In SOT-23-6 Mar 26, 2009 · The MCP4725 Is A Low-power, High Accuracy, Single Channel, 12-bit Buffered Voltage Output Digital-to-Analog Convertor (DAC) With Non-volatile Memory (EEPROM). Its On-board Precision Output Amplifier Allows It To Achieve Rail-to-rail Analog Output Swing. The DAC Input And Configuration Data Feb 26th, 2024.

Using The EEPROM Memory In AVR-GCC Using The EEPROM Memory In AVR-GCC Tutorial (c) Dean Camera, 2006. Dean_camera@hotmail.com What Is The EEPROM Memory And Why Would I Use It? Most Of The AVR's In Atmel's Product Line Contain At Least Some Internal EEPROM Memory. EEPROM, Short For Electronically Erasable Read-Only Memory, Is A For Mar 3th, 2024 Using The EEPROM Memory In AVR-GCC - GitHub Now Provided By The Module Of Avr-libc. There Are Ve Main Types Of

EEPROM Access: Byte, Word, Dword, Oat And Block. Each Type Has Three Types Of Functions; A Write, An Update, And A Read Variant. Jan 19th, 2024Zed-Full Key Programmer EEprom Application Price List Jan ...Zed-Full Eeprom Application List
Www.abkeys.com 20 01 2021 Package Name Standard Description Supported Models Credit Price \$ Price DHS EEPROM038 Yes TOYOTA, LEXUS ECU FUJITSU 4C TRANSPONDER, Jan 12th, 2024.

Immo Eeprom Programmer - Chords.rukeba.comSeptember 8th, 2020 - Iprog ECU Programmer Updated To V82 This Article Is Available With Iprog V82 Free Download Link And Win7 Installation Guide Test Reports Etc Iprog ECU Programmer Is A Powerful Tool Can Do 1 Airbag Airbag Read And Erase Crash To Some Cars Read And Erase DTC Repair CFG 2 Car Functions Car Radio DPF OFF ECU IMMO Key Mar 10th, 2024Supervisory Circuits With EEPROM Serial 16-Kb I2C ...With Brown Out Protection And A Watchdog Timer Are Integrated Together In Low Power CMOS Technology. Memory Interface Is Via An I2C Bus. The 1.6-second Watchdog Circuit Returns A System To A Known Good State If A Software Or Hardware Glitch Halts Or “h Jan 30th, 2024Supervisory Circuits With I2C Serial CMOS EEPROM ...The 1.6-second Watchdog Circuit Returns A System To A Known Good State If A Software Or Hardware Glitch Halts Or “hangs” The System. The CAT1161 Watchdog Monitors The SDA Line, Making An Additional PC Board Trace Unnecessary. The Lower Cost CAT1162 Does Not Have A Watchdog Timer. The Power S Feb 2th, 2024. Two-wire Serial EEPROMNote: 1. V IL Min And V IH Max Are Reference Only And Are Not Tested. Table 2-1. Pin Capacitance(1) Applicable Over Recommended Operating Range From T A = 25°C, F = 1.0 MHz, V CC = +1.8V Symbol Test Condition Max Units Conditions C I/O Input/Output Capacitance (SDA) 8 PF V I/O = 0V C IN Input Capacitance (A 0, A 1, A 2, SCL) 6 PF V IN = 0V ... Mar 16th, 2024

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