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Definitions Of LOW And HIGH In Our Discussions Of TTL Circuit Behavior: LOW 0–0.8  
Volts. HIGH 2.0–5.0 Volts. TTL.1 Basic TTL NAND Gate The Circuit Diagram For A  
2-input LS-TTL NAND Gate, Part Number 74LS00, Is Shown In Figure Mar 12th, 2024.  
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